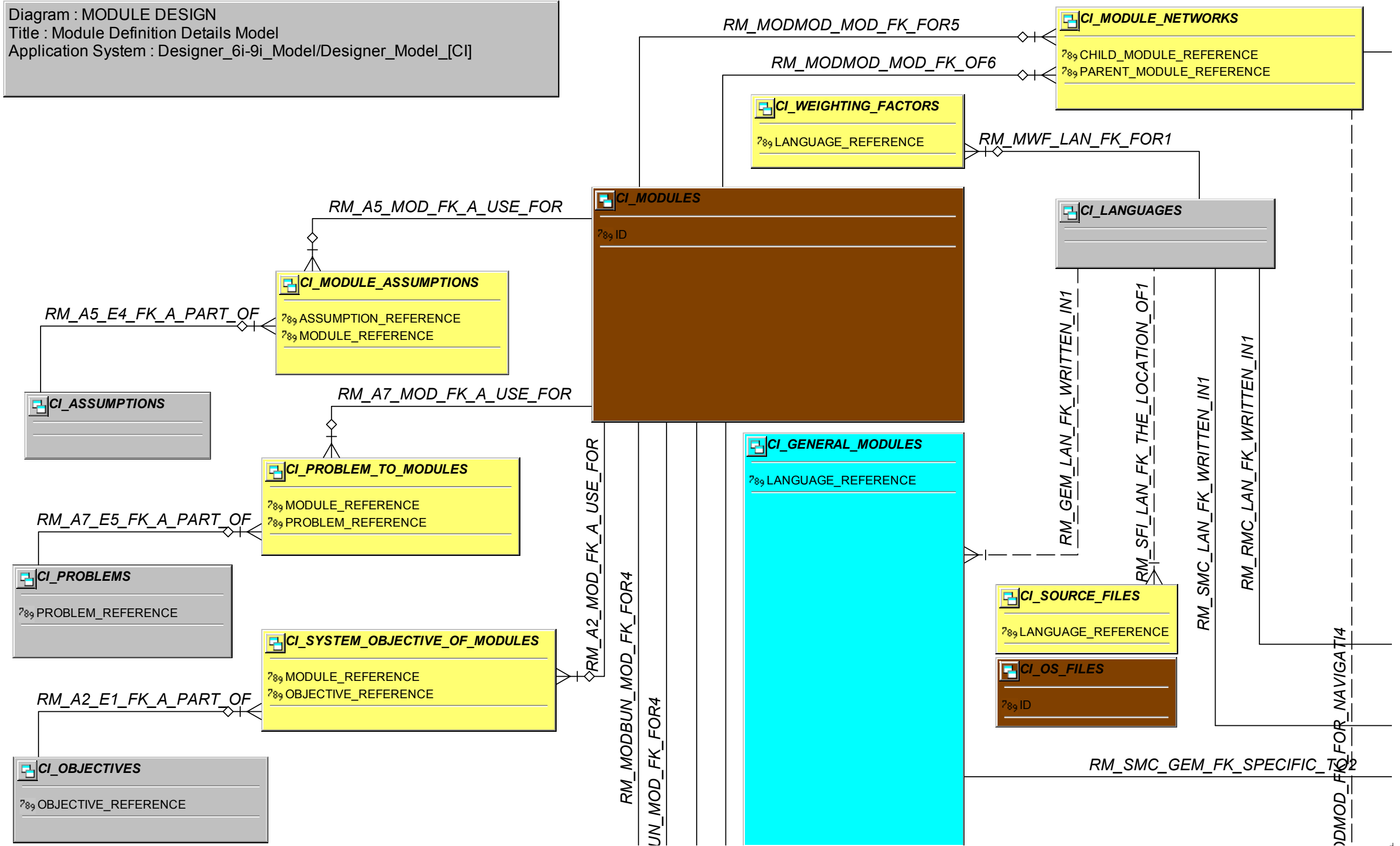
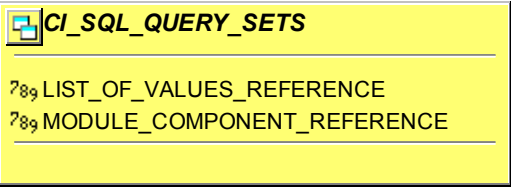
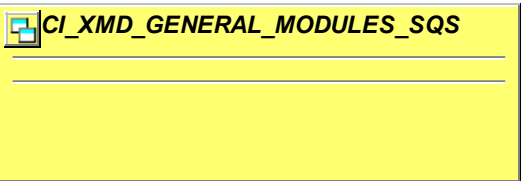
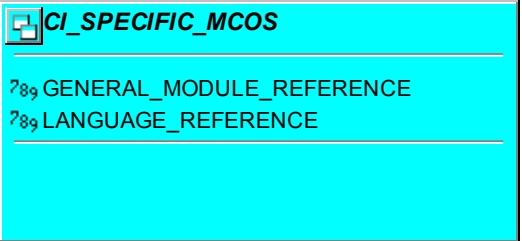
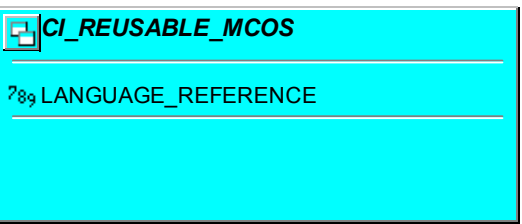
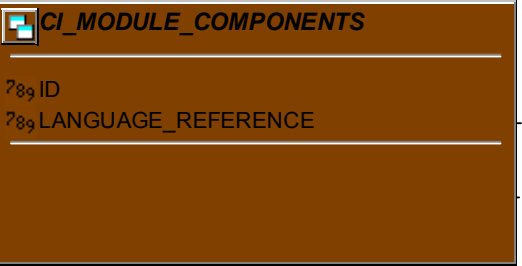
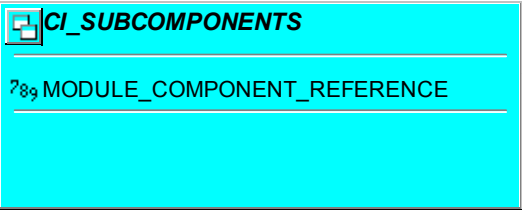
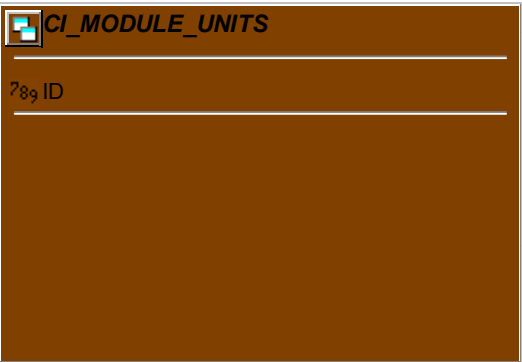


Diagram : MODULE DESIGN
Title : Module Definition Details Model
Application System : Designer_6i-9i_Model/Designer_Model_[CI]



RM_PVL_MODMOD_FK_PASSED_VIA4

RM_UBI



RM_IGR_SUB_FK_WITHIN5

RM_IGR_MCO_FK_WITHIN2

RM_SQS_MCO_FK_IN_SET_OPERATION2

RM_MCN_MCO_FK_THE_USAGE_OF4

RM_MTI_MUN_FK_WITHIN13

RM_SQS_LOV_FK_INSET_OPERATION3

RM_MTI_SQS_FK_WITHIN16

RM_D

RM_MTI_MTI_FK_LINKED_FR

DM KRI MTI FK TO

RM_MCN_SUB_FK_THE_USAGE_OF7

RM_SUB_RMC_FK_SUBCOMPONENT_IN2

RM_SUB_SMC_FK_SUBCOMPONENT_IN2

